1020020040829

FIG. 1

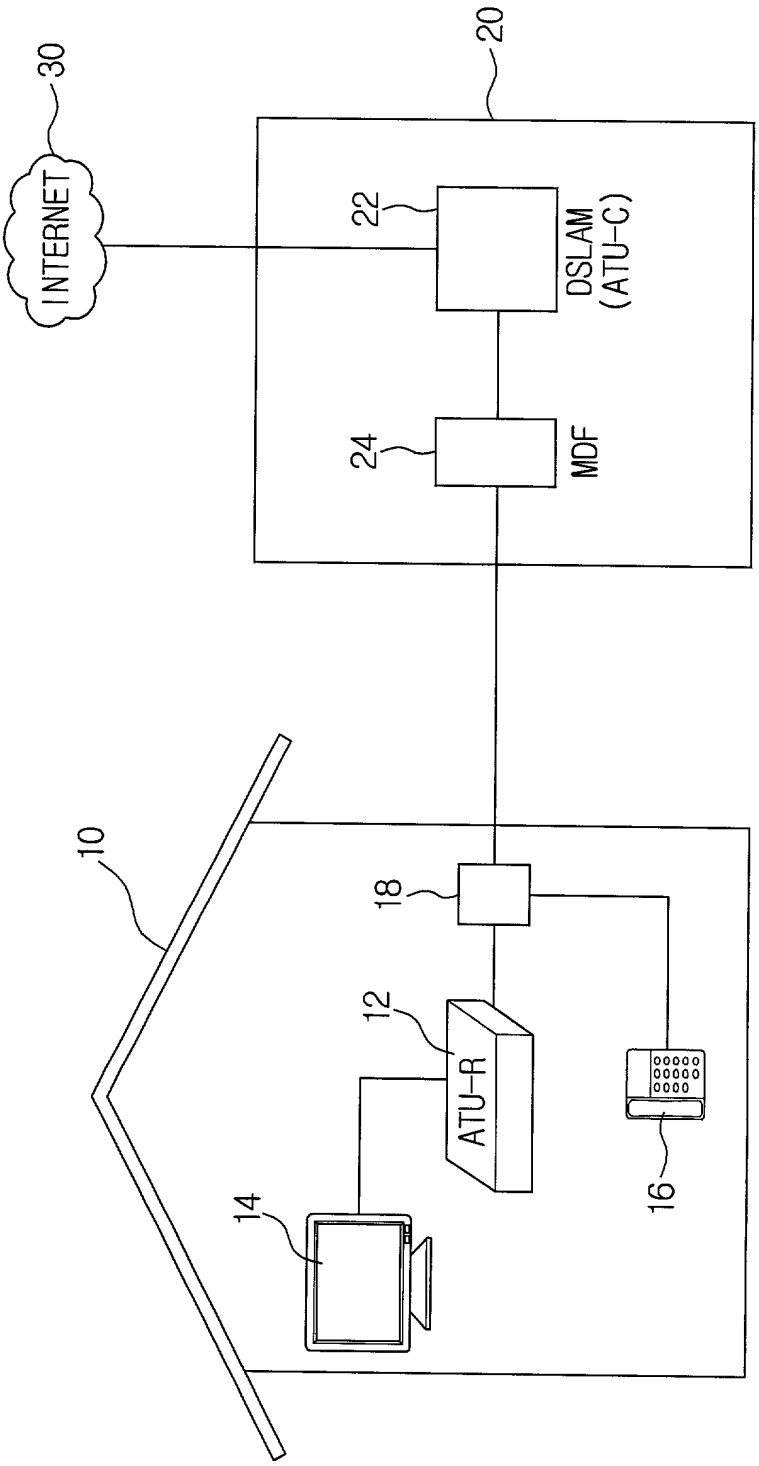


FIG.2

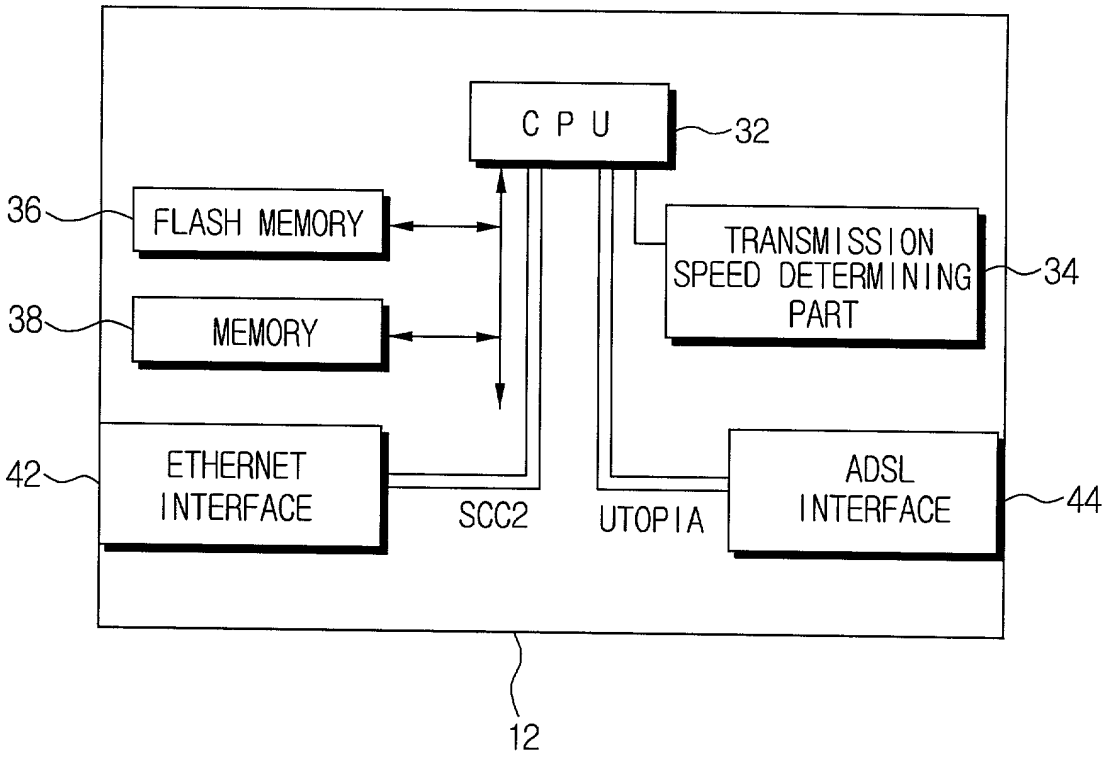

C P U
32
36
FLASH MEMORY
38
MEMORY
TRANSMISSION SPEED DETERMINING PART
34
42
ETHERNET INTERFACE
SCC2
UTOPIA
ADSL INTERFACE
44
12

FIG.3

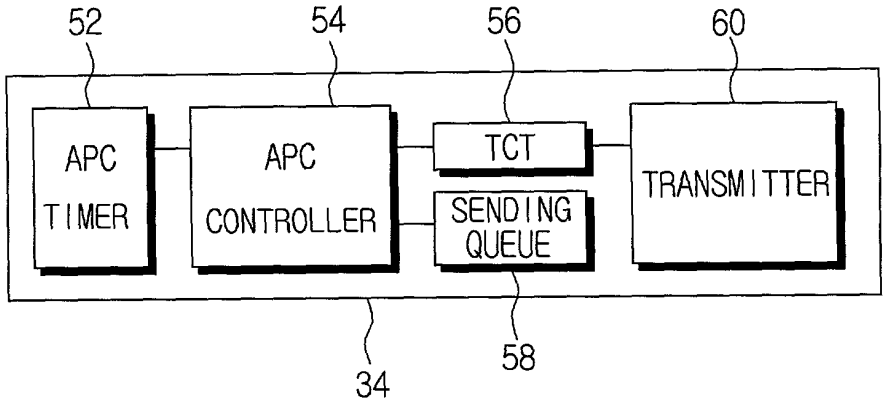

52
54
56
60
APC TIMER
APC CONTROLLER
TCT
SENDING QUEUE
TRANSMITTER
34
58

FIG.4

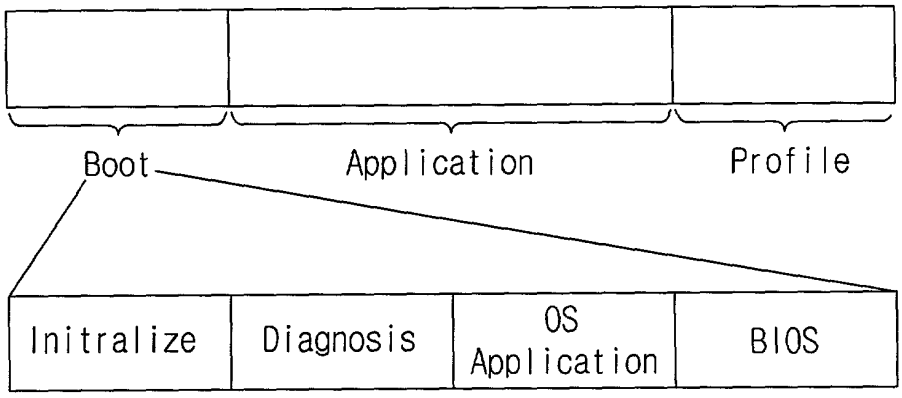
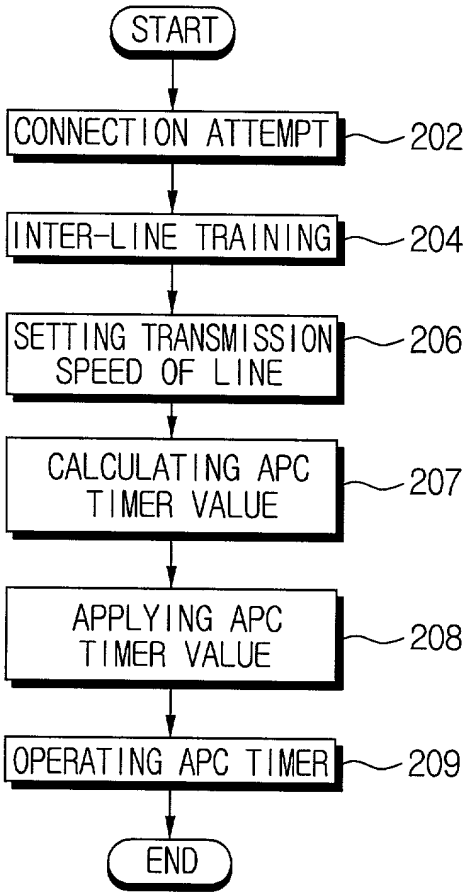

Boot
Application
Profile
Initralize
Diagnosis
OS Application
BIOS

FIG.5


START
CONNECTION ATTEMPT
202
INTER-LINE TRAINING
204
SETTING TRANSMISSION SPEED OF LINE
206
CALCULATING APC TIMER VALUE
207
APPLYING APC TIMER VALUE
208
OPERATING APC TIMER
209
END